



bitvis

Leading independent design centre
for Embedded SW and FPGA

Newsletter

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News

- [Bitvis is now member of EtherCat Technology Group](#)
- [Cooperation with Realtime Embedded in Sweden](#)
- [Guest Lecture and Presentation in Bergen](#)
- [Keynote++ at ABB's Embedded Systems Forum](#)
- [Bitvis Utility Library v2.3.0 is now available](#)
- [Presentations on making good testbenches now released as PPTs](#)

Recommended reading on SW and FPGA

[Introduction to generic programming](#)

Generic programming is about writing your code portable, modular and easily maintainable. There are however limits for how much effort you want to put into making your code generic. This article will give an overview of different levels of genericity as well as some guidelines to follow.

["UVM for VHDL" - UVVM: Universal VHDL Verification Methodology](#)

Complex designs often need advanced testbenches. SystemVerilog and UVM are being pushed as "the solution", but VHDL and selected libraries is most often a far better solution.

[VHDL simulation transcript is important for quality and efficiency](#)

The structure of your testbench is important, but so is the progress information being produced by that testbench.

[Complete list of Presentations and Articles.](#)

Seminars and Events

- 09. Oct 2014 [X-fest \(Xilinx\)](#)
- 19-20 Nov 2014 [FPGA Development Best Practices, Stockholm](#)
- 11-12. Feb 2015 [FPGA-forum](#)

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